



## **Dr. Anilkumar V. Nandi**

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### **Objective**

To contribute to the Engineering of VLSI / Embedded Systems Education and Research.

### **Experience**

**June 1991 – Till date**

**Electronics and Communication Engineering, B.V.B. College of Engineering and Technology, Hubli, Karnataka.**

### **Academic Profile**

#### **Sept 2010 – Till date; Professor**

- Controller of Examinations.
- Teaching the subject 'Low Power VLSI Circuits' for PG, 'Advanced VLSI Circuits' for UG.
- Coordinator of Value Addition Training Cell since 4 years. Motivated faculty, initiated many new programs across all the departments in the institute, and put in place a good revenue generation model.
- Guiding 4 Ph.D. students.
- Initiated UG Diploma program in Embedded Systems – a finishing school for UG students and a revenue generation model for college
- TEQIP coordinator for Training and Equity plan
- Chairman ISTE Chapter in the Institute.
- PG Coordinator for 'VLSI Design and Testing'
- Introduced the subject MEMS in autonomous curriculum
- Published **8** Research papers in journals (See Annexure A)
- Published **37** papers in National (**16**) and International (**21**) Conferences (See Annexure B)
- Resource person for workshops and conferences (See Annexure C)
- Guiding UG and PG projects (Important projects – See Annexure D)
- Active Member of NPMASS Program at the institute (NPMASS – National Program of Micro and Smart Structures, a scheme under under MHRD, Govt. of India, headed by IISc, Bangalore at National level)
- Organized number of workshops and seminars (See Annexure E)

#### **April 2003 – August 2010; Assistant Professor**

- Guided UG and PG projects

- Taught subjects related to VLSI, MEMS & Embedded Systems

**Nov 1999 – March 2003; Senior Lecturer**

- Taught subjects Electronic circuit design, Analog electronics, Linear ICs. Guided UG and PG projects

**Aug 1991 – Oct 1999; Lecturer**

- Taught subjects Electronic circuit design, Analog electronics, Linear ICs, Information Theory and Coding. Guided UG and PG projects

**1988 June – 1991 June, Zenith Computers Limited, Mumbai**

**Systems Development Engineer**

- Installation and Commissioning of NOVELL NETWARE Networking Systems
- Brought the down-time of networked systems to minimum and was awarded “**Best Performance Award**”
- Got appreciations from many clients for quick troubleshooting and reducing the down-time of networked systems to minimum time.

**Education**

**2004 Nov–2010 Sept; Visvesvaraya Technological University, Belgaum**

- **Ph.D. (Electrical and Electronics Engineering)** in the domain of VLSI Architectures and Embedded Systems
- Thesis title: Spatial Prediction based Optimized Architecture for Image Compression

**2001 June – 2002 Dec; Indian Institute of Technology, Kharagpur, West Bengal**

- **M.Tech. (Microelectronics and VLSI Design)**
- Thesis title: Design and Development of Piezoresistive Accelerometer for Avionics Applications (A DRDO sponsored project)

**1984 June – 1988 June; B.V.B. College of Engineering and Technology, Hubli, Karnataka**

- **B.E. (Electronics and communication Engineering)**
- Affiliated to Karnataka University
- Project title: Design and Implementation of Digital Filter using Bit-Slice Microprocessors.

**Industry Associations for projects and Trainings**

**For projects**

- TATA Elxsi, Bangalore
- Texas Instruments, Bangalore
- Sankalp Semiconductor, Hubli

**For Trainings**

- Unified Softech Pvt. Ltd., Hubli
- NGX Technologies, Bangalore

**Professional Memberships and contributions**

**Memberships**

- Member of Institution of Engineers (India)
- Life Member of Indian Society of Technical Education (ISTE)

- Member of Semiconductor Society of India
- Member of Systems Society of India
- Member of International society IACSIT (International scientific association of distinguished scholars engaged in Computer Science and Information Technology)
- **Teacher Member of Indian Academy of Sciences, Bangalore**
- **Mentor/Resource person for Summer/Winter Camps and other INSPIRE initiatives of Department of Science and Technology (DST), MHRD, Govt. of India.**

#### **Contributions**

- Member of LSWA Library since 12 years
- Honorary Treasurer of LSWA Library since 5 years
- Faculty in-charge of ISTE chapter in the institute
- Member – Board of Studies in Automation and Robotics Engineering, B.V.B.College of Engg. & Tech., Hubli, since the inception of the course (2009-10.)
- **Reviewer for VLSI Design and Test (VDAT) since 2005.**
- Participated in the beta-test phase of the new MSP430 Teaching ROM by Texas Instruments, USA. Proud to be part of a world wide panel of experts.
- Contributed for an **International edition of the book, “Foundations of MEMS”**, by Chang-Liu, Pearson Education.
- Contributed for an **International edition of the book, “Analog Integrated Circuits”**, by Carusone, John Wiley Publication, USA.
- Associate editor of the following 'bioinfo' journal publications
  - 1) **Journal of Biomedical and Bioengineering, ISSN: 0976 – 8084 & E-ISSN: 0976–8092**
  - 2) **Journal of Signal and Image Processing, ISSN: 0976-8882 & E-ISSN: 0976-8890**
- Reviewer for International Journal of Engineering and Technology (IJET) a journal publication of IACSIT.

#### **Institute / Professional Society Associations /Funding/Grant**

#### **Associations**

- NPMASS program of IISc for design and fabrication of MEMS devices
- VLSI Society of India for VDAT symposium activities
- Awarded Fellowship of Indian Academy of Sciences, Bangalore for research work during 2005
- Faculty coordinator for ISTE Students Chapter in the institute

#### **Funding/Grant**

- Co-coordinator for MODROB scheme (Rs 6 Lakhs) for the year 2010-12 for developing Advanced Microcontroller laboratory
- Received MSP430 development kits from Texas Instruments for carrying out project work
- Chief Coordinator of Industry-Institute Partnership Cell (IIPC). Received grant for Rs. 6 Lakhs for IIPC from AICTE, New Delhi for the period 2011-2013.

#### **Interests**

**Networking with people, Playing Lawn-Tennis, Learning Education Laws in India.**

# Annexure A

## Journal publications

- [1] Anil V. Nandi, Vaishali B.M. Deepak Giriraddi “PC based dual beam Colorimeter,” Proceedings of National Symposium on Instrumentation, jointly organized by **Instrumentation Society of India, IISc** Bangalore and National Physical Laboratories, New Delhi, 1999. Also published in **Journal of Instrumentation Society of India-1997, 28(3)**
- [2] Nagaveni Doddamani, Anil V. Nandi, Harishchandra, “Design and Implementation of SPDT switch, phase shifter and attenuator for L band T/R module using 0.7um MMIC technology,” **IETE Journal of Research 2006.**
- [3] Anil V. Nandi, R.M.Banakar, and L.M.Patnaik, “VLSI implementation of Spatial Prediction based Image Compression Scheme,” IEEE International Conference on Industrial and Information Systems ICIIS 2006], University of Peradeniya, Sri Lanka, 8-11 Aug 2006, pp. 338-343. 1-4244-0322-7/06 \$20, 2006 **IEEE (IEEEExplorer)**
- [4] Anil V. Nandi, R.M.Banakar, L.M.Patnaik, “Memory Efficient Spatial Prediction based Image Compression Scheme,” **Elsevier journal of Image and Vision Computing**, Vol.25, 2007, pp. 899-906. doi:10.1016/j.imavis 2006.07. 003.
- [5] Anil V. Nandi, R.M.Banakar, “Parallel implementation of SPIHT algorithm for compression of images,” Proceedings of International Conference on Industrial and Information Systems ICIIS 2007, University of Peradeniya, Sri Lanka, 9-12 Aug 2007, pp. 329-333. 1-4244-3241-7/07/ \$25.00, 2007 IEEE (**IEEEExplorer**)
- [6] Anil V. Nandi, R.M.Banakar, “Parallel implementation of SPIHT algorithm for compression of images,” Proceedings of International Conference on Industrial and Information Systems ICIIS 2007, University of Peradeniya, Sri Lanka, 9-12 Aug 2007, pp. 329-333. 1-4244-3241-7/07/ \$25.00, 2007 IEEE (**IEEEExplorer**)
- [7] Anil V. Nandi, R.M.Banakar, “Throughput efficient Parallel implementation of SPIHT Algorithm,” Proceedings of 21<sup>st</sup> IEEE International Conference on VLSI Design and Embedded Systems, HiCC, Hyderabad, 4-8 January 2008, pp. 482-486. (**IEEEExplorer**)  
1063-9667/08 \$25.00, 2008 IEEE, DOI: 10.1109/VLSI. 2008.48, IEEE Computer Society.
- [8] Anil V. Nandi, R.M.Banakar, “Efficient Architecture for SPIHT Algorithm for Compression of Images,” **Transactions on Electronics and Communications**, International Symposium on Electronics and Telecommunications ISETC 2008, Timisoara, Romania, September 2008, pp. 150-154. Tom 53(67), Fascicola 1-2, 2008.
- [9] Rashmi Pawar, Anilkumar V. Nandi, A. S. Jadhav, “Variable Sorting Threshold Based SPIHT Algorithm for Image Compression”, **MES Journal of Technology & Management** Vol. 01, NO.02, Special Issue on “International Conference on Computational Engineering, Practices and Techniques”, November 2010, INDIA, pp. 83-88, ISSN 0976-3724
- [10] Rashmi Pawar, Sangeeta Angadi, Anil V. Nandi, “Directional Spatial Prediction Image Compression”, **MES Journal of Technology & Management** Vol. 01, NO.02, Special Issue on “International Conference on Computational Engineering, Practices and Techniques”, November 2010, INDIA, pp. 140-145, ISSN 0976-3724

# Annexure B

## Papers published in National and International Conferences

[1] Anil V. Nandi, "PC-Phone," Proceedings of National conference on Advances in Broadband Communications NCABC-98, organized jointly by IETE, Maharashtra state and Walchand College of Engg., Sangli, Maharashtra, 6-7 April 1998, pp. E4-E7.

[2] Anil V. Nandi, Vaishali B.M. Deepak Giriraddi "PC based dual beam Colorimeter," Proceedings of National Symposium on Instrumentation, jointly organized by **Instrumentation Society of India, IISc** Bangalore and National Physical Laboratories, New Delhi, 1999.

Also published in **Journal of Instrumentation Society of India-1997, 28(3)**

[3] Anil V. Nandi, "MEMS Development and a case study of accelerometer," Proceedings of the workshop at Electrical and Electronic Communication Engineering, Indian Institute of Technology, Kharagpur, 27-28 Sept 2002.

[4] Anil V. Nandi, Soumen Das, S.K.Lahiri, "Design and Development of PZR accelerometer," Proceedings of **VLSI Design and Testing**, VDAT 2003, Infosys campus, Mysore, Aug 2003.

[5] Anil V. Nandi, Mahesh Kamat, Gopal V. Kuljkarni, "Design and implementation of pH Controller for PCB industry," Proceedings of National Symposium on Instrumentation, NSI-28 organized jointly by **Instrumentation Society of India, IISc** Bangalore and G.B.Pant University of Agriculture and Technology, Uttaranchal, 3-5, Nov 2003, pp. 97.

[6] Anil V. Nandi, Inderjeet Singh Sublok, "Voice over Internet Protocol," Proceedings of National Conference on Intelligent Systems and Networks ISN-2004, Haryana Engineering College, Jagadhri, Haryana, 27-28 Feb 2004.

[7] Anil V. Nandi, Vaishali B.M., "Modeling of RF MEMS using Sugar tool," Proceedings of International Conference on Instrumentation and Control INCON 2004, organized jointly by **Instrumentation Society of India** & College of Engineering, Pune, September 2004, pp.28-35.

[8] Anil V. Nandi, R.M.Banakar, "VLSI Architectures for Image Processing," Asia pacific Conference on Parallel and Distributed Computing Technologies, ObComAPC, Vellore Institute of Technology, Vellore, Tamilnadu, India, 13-15 December, 2004, pp. 483-488.

[9] Priyanka G, Shalini Neeli, Trupti R, Kavya P, Anil V. Nandi, R.M.Banakar, "Image Codec using Wavelet/Set Partitioning In Hierarchical Tree approach," Proceedings of National Conference on Signal Processing and Communication NCSPC 2006,7-8 July 2006, Jawaharlal Nehru National College of Engineering, Shimoga, Karnataka, India, pp. 81-89.

- [10] Anil V. Nandi, R.M.Banakar, and L.M.Patnaik, "VLSI implementation of Spatial Prediction based Image Compression Scheme," IEEE International Conference on Industrial and Information Systems ICIIS 2006), University of Peradeniya, Sri Lanka, 8-11 Aug 2006, pp. 338-343. 1-4244-0322-7/06 \$20, 2006 **IEEE (IEEEExplorer)**
- [11] Anil V. Nandi, R.M.Banakar, "VLSI implementation of modified addressing scheme SPIHT Image Coder/Decoder," Proceedings of **IEEE India Conference** INDICON 2006, 16-18 September 2006, New Delhi. (**IEEEExplorer**)
- [12] Anil V. Nandi, Vaishali B.M. "Hierarchical modeling of RF MEMS," Proceedings of International Symposium on Communications and Information Technologies ISCIT 2006, September 2006.
- [13] Anil V. Nandi, R.M.Banakar, "Reduced bit rate Image Codec using Wavelet/Set Partitioning In Hierarchical Tree approach," International Conference of Signal and Image Processing ICSIP 2006, 8-11 Dec 2006, B.V.B.College of Engg., Hubli, pp. 84-88.
- [14] Anil V. Nandi, R.M.Banakar, "Efficient Memory sorting SPIHT Image Coder/Decoder," 3<sup>rd</sup> International Conference on Computers and Devices for Communication CODEC-06, Institute of Radio Physics and Electronics, University of Calcutta, 18-20 December 2006, pp. 166-169.
- [15] Sushma Hiremath, Anil V. Nandi, R.M.Banakar, "Performance analysis of SPECK Progressive image compression algorithm," 12<sup>th</sup> IEEE Symposium on Advances in Information Technology, Central Power Research Institute, Bangalore, 8-11 Dec 2006,
- [16] Anil V. Nandi, Sujata Patil, "Performance evaluation of 1-D systolic array," Proceedings of International Conference on Signal and Communication Networks ICSCN 2006, Dec 2006 (IEEEExplorer)
- [17] Nagaveni Doddamani, Anil V. Nandi, Harishchandra, "Design of SPDT switch, phase shifter and attenuator for L band T/R module using 0.7um MMIC technology," Proceedings of International Conference on Signal and Communication Networks ICSCN 2006, Dec 2006 (IEEEExplorer)
- [18] Anil V. Nandi, "Design and Hardware Modeling of FFT Processor using Systolic Array Architecture," Fourth International conference on 'Bridging the Digital Divide' Asian Applied Computing Conference AACC 2007, Kathmandu, Nepal, 8-10 June 2007.
- [19] Vaishali B.M, Uday V. Wali, Anil V. Nandi , "Structured Design and Simulation of RF MEMS resonator," Fourth International conference on 'Bridging the Digital Divide' Asian Applied Computing Conference AACC 2007, Kathmandu, Nepal, 8-10 June 2007.

[20] Anil V. Nandi, Nagaveni Doddamani, Harishchandra, "Design and Implementation of SPDT switch, phase shifter and attenuator for L band T/R module using 0.7um MMIC technology," **IETE Journal of Research** 2006.

[21] Anil V. Nandi, R.M.Banakar, L.M.Patnaik, "Memory Efficient Spatial Prediction based Image Compression Scheme," **Elsevier journal of Image and Vision Computing**, Vol.25, 2007, pp. 899-906. doi:10.1016/j.imavis.2006.07.003.

[22] Anil V. Nandi, R.M.Banakar, "Parallel implementation of SPIHT algorithm for compression of images," Proceedings of International Conference on Industrial and Information Systems ICIIS 2007, University of Peradeniya, Sri Lanka, 9-12 Aug 2007, pp. 329-333. 1-4244-3241-7/07/ \$25.00, 2007 IEEE (**IEEEExplorer**)

[23] Anil V.Nandi, "Reconfigurable Computing," Pre-Conference Workshop proceedings of International Conference on Industrial and Information Systems ICIIS 2007, Sri Lanka, 9-12 Aug 2007.

[24] Anil V. Nandi, Shivanand Hungund, "Fill Gun – A data storage for Military applications," Proceedings of International Conference on Embedded Systems and Communications, ICEMC<sup>2</sup>, Bangalore, Aug 2007

[25] Anil V. Nandi, R.M.Banakar, "Throughput efficient Parallel implementation of SPIHT Algorithm," Proceedings of 21<sup>st</sup> International Conference on VLSI Design and Embedded Systems, HiCC, Hyderabad, 4-8 January 2008, pp. 482-486. (**IEEEExplorer**) 1063-9667/08 \$25.00, 2008 IEEE, DOI: 10.1109/VLSI.2008.48, IEEE Computer Society.

[26] Anil V. Nandi, R.M.Banakar, "High-Speed, High-Throughput Pipelined and Parallel Architecture for SPIHT Algorithm," Proceedings of 12<sup>th</sup> IEEE VLSI Design and Testing Symposium VDAT 2008, Bangalore, India, 23-26 July 2008, pp. 434-438.

[27] Anil V. Nandi, R.M.Banakar, "Efficient Architecture for SPIHT Algorithm for Compression of Images," **Transactions on Electronics and Communications**, International Symposium on Electronics and Telecommunications ISETC 2008, Timisoara, Romania, September 2008, pp. 150-154. Tom 53(67), Fascicola 1-2, 2008.

[28] Sunita Shirahatti., Rohini Hongal, Rohini H. Korti, Anil V. Nandi, "SRAM Memory design with optimized column decoder using PTL," National Level Technical Symposium PRAGATI-08, for PG/Research Students Conducted by Dept. of E & C, B. V. Bhomaraddi College of Engg. and Tech., Hubli, on 18th October 2008.

[29] Sunita Shirahatti., Rohini Hongal, Rohini H. Korti, Anil V. Nandi, "Column Decoder using PTL for Memory," International Conference on Advanced Communication and Informatics organized by Department of Electronics and Communication Engineering, Thanthai Periyar Government Institute of Technology, Vellore, Tamilnadu, from 11-13 January, 2009 ICACI-2009, pp 9-14.

[30] Anil V. Nandi, Vaishali B.M., Shivanand B., Shivaprasad G.C., et.al., "Wireless Ambulatory Blood Pressure Monitoring System," A Bi-monthly News letter from Texas Instruments UniTI, Vol.2, Issue 3, July 2009, pp.7-8.

[31] Anil V. Nandi, R. M. Banakar and L. M. Patnaik, "Efficient Address Generation Sub System for SPIHT based Memory-Efficient Image Compression Scheme," Proceedings of The 2009 International Symposium on Computing, Communication, and Control ISCCC 2009, NEC, Nanyang Technological University, Singapore, 9-11 October 2009, pp. 174-179.

[32] Sunita Shirahatti, Anil V. Nandi," *A Capacitorless low dropout regulator,*" International conference on Communication Control and computing Technologies - ICCCT'10 organized by St. Xaveirs Catholic College of Engineering Chunkankadai, from 07-09 October, 2010 ICCCT'10, pp 256-263.

[33] Rashmi Pawar, Anilkumar V. Nandi, A. S. Jadhav, "Variable Sorting Threshold Based SPIHT Algorithm for Image Compression", **MES Journal of Technology & Management** Vol. 01, NO.02, Special Issue on "International Conference on Computational Engineering, Practices and Techniques", November 2010, INDIA, pp. 83-88, ISSN 0976-3724

[34] Rashmi Pawar, Sangeeta Angadi, Anil V. Nandi, "Directional Spatial Prediction Image Compression", **MES Journal of Technology & Management** Vol. 01, NO.02, Special Issue on "International Conference on Computational Engineering, Practices and Techniques", November 2010, INDIA, pp. 140-145, ISSN 0976-3724

[35] Sujata Patil, Anil V. Nandi, "Performance Evaluation Of one dimensional Systolic Array For FFT processor using pipelining," Proceedings of International Conference on Communication, Computation, Control and Nanotechnology ICN-2010, 29-30 October 2010 organized by REC, Bhalki, Karnataka.

[36] Contributed for an **International edition of the book, "Foundations of MEMS"**, by Chang-Liu, Pearson Education, USA.

[37] Summaiyya S. Mirji, Anil Nandi, Jyoti Bali, "Implementation of ECG Monitoring and Heart Rate Measurement System," Proceedings of National Conference on Assistive Technology "Making Technology an integral part of the Differently Abled", 29-31 July 2011, at Padmasri Dr.B.V.Raju Institute of Technology (BVRIT), Andhra Pradesh, India. **Won Best Paper Award.**



## Annexure C

### Resource person for workshops and conferences

[1] “Micro-Electro-Mechanical Systems (MEMS) introduction and a case study of PZR Accelerometer” during workshop at IIT, Kharagpur during 22-23 September 2002,

[2] “Introduction to MEMS” and “A case study of PZR Accelerometer” during workshop at BVBCET, Hubli during 7-8 November 2003.

[3] A talk on “Finite Element Method applications in MEMS” at SDM Engineering College, Dharwad on 7<sup>th</sup> April 2004.

[4] “Reconfigurable Computing” during Pre-conference workshop at International Conference on Industrial and Information Systems 2007 ICIIS-2007 at University of Peradeiniya, Sri Lanka, 10-13 August 2007.

[5] “Micromachining Techniques in MEMS” at DST sponsored workshop on Nanotechnology in BVBCET, Hubli on 14<sup>th</sup> July 2005.

[6] “FPGA fundamentals” during the workshop on “Digital Design using FPGA” at BVBCET, Hubli during 12-14 February 2005.

[7] “Overview of 802.15.4/ZigBee Wireless Standard and a case study” during Visvesvaraya Technological University [VTU] and Vision Group on Science and Technology [VGST], Govt. of Karnataka Sponsored Faculty Development Program on “Advances in Wireless Communications” organized at Basaveshwar Engineering College, Bagalkot during 10-12 February 2011.

[8] A Key-note talk on “Overview of MEMS – An Emerging Technology” during Visvesvaraya Technological University [VTU] and Vision Group on Science and Technology [VGST], Govt. of Karnataka Sponsored Faculty Development Program on “Modeling, Simulation and Fabrication of MEMS Devices” organized at B.V.B.College of Engineering and Technology, Hubli during 10-12 March 2011.

[9] A talk on “ARM7 Microprocessor/Controller and its applications”, at Hirasugar Institute of Technology, Nidsoshi, Belgaum district on 14<sup>th</sup> March 2011 for Pre-final and Final year students of electrical sciences.

# Annexure D

## Project Recognitions

### Under-Graduate

- [1] "Hardware Modeling of SPIHT algorithm for compression of images", Sponsored by KSCST, Bangalore.
- [2] "Wireless Ambulatory Blood Pressure Monitoring", Sponsored by Texas Instruments, Bangalore.
- [3] "Virtual Hands – A tool for the disabled for using Computers", Sponsored by Texas Instruments, Bangalore.
- [4] "NAND Flash Controller – Design and Implementation", Sponsored by TATA Elxsi, Bangalore.
- [5] "Rate-Distortion Optimizer for Audio compression", Sponsored by TATA Elxsi, Bangalore.
- [6] "MSP430 based Automated Wheelchair- An Aid for physically challenged people" is selected in **top 17 projects** in Phase-1 round of National level Texas Instruments' Analog Design (TI-ADC) Contest for the year 2010.

### Post-Graduate

- [1] Project titled "Capacitor-less low dropout regulator," is selected in **Top 11 Projects** in Final round of **National level Cadence Design Contest** for the year 2009.
- [2] Paper on Project titled "Implementation of ECG Monitoring and Heart Rate Measurement System," at National Conference on Assistive Technology "Making Technology an integral part of the Differently Able", 29-31 July 2011, at Padmasri Dr.B.V.Raju Institute of Technology (BVRIT), Andhra Pradesh, India has **won Best Paper Award**.

# Annexure E

## Workshops organized

- [1] MEMS workshop at BVBCET during 7-8 November 2003.
- [2] "Digital Design using FPGA" at BVBCET during 12-14 February 2005.
- [3] "Embedded Systems using MSP430" at BVBCET on 18<sup>th</sup> May 2006 in association with Gill Instruments, Bangalore.
- [4] "Embedded Systems and RTOS" at BVBCET on 15<sup>th</sup> November 2010 in association with TATA Elxsi, Bangalore.
- [5] **Initiated UG Diploma in Embedded Systems** in BVBCET (during summer / winter vacations) in association with Microcore solutions Bangalore, NGX Technologies Bangalore and Unified Softech Hubli. **It is a continuously on-going program in BVB.**