

BIODATA

Name	Dr. VIJAYA C
Qualification	BE in E&C with first class from B.V.B College of Engg & Tech., Hubli, 1988.
	M. Tech in Digital Electronics and Communication Systems with first class, distinction from Malnad College of Engineering, Hassan, 1991.
	Ph. D on 'Some studies on Digital Signal Processing' under the guidance of Dr. J. S. Bhat, Dept of Physics, Karnatak University, Dharwad, Jan 2008.
Teaching Experience	18 years Worked as Lecturer in CS Dept, KHK Institute, Dharwad, from 1988-1989. Worked as Lecturer in E&CE Dept, SDM College of Engineering & Technology, Dharwad, from 1991-2004. Promoted as Asst. Prof. in 2004. Head of the Department from 06-07-2007 to 05-07-2009. Promoted to Associate Prof. in May 2008. Promoted to Prof. in March 2010. I/C Head of the Department from 10-06-2010 Head of the Department from 07-08-2010 Member of BOG in College
Awards at the College Level	1. 'Best Teacher Award', 2008-2009 2. 'Special Appreciation for Research Publications', 2008-2009
Books Published	Coauthored a book on 'Digital Signal Processing' in 2004-05.
Ph. D. Scholars	1. Mrs. Sharada C. Sajjan, Speech signal processing 2. Mr. Milind Rane, Biometric fusion 3. Mrs. Renuhka H. Korti, Cryptography
Papers published in Journals	1. 'Signal compression Based on Discrete Fractional Fourier Transform and SPIHT', Elsevier Science Publications, Signal Processing, pp. 1976-1983, Vol. 86, Aug. 2006. 2. 'Sliding discrete fractional transforms', Signal Processing, pp. 247-254, vol. 88, Feb 2008.
Research Papers presented	1. "DSP Processors", Proc.of Two-day Seminar on Digital Signal Processing, 24-25 th Jan. 2001, at Karnataka University, Dharwad, pp. 48-49.
	2. "Signal compression based on DFRFT and SPIHT", J. S. Bhat and Vijaya C, Proc. of National Conference on ECCS-2004, 23-24 th Sept. 2004, at TIET, Patiala, pp. 187-190.
	3. "Perceptually lossless Audio Codec based on DFRFT and SPIHT", J. S. Bhat and Vijaya C, Proc. of 13 th International Conference on ADCOM-2005, 14-17 th Dec. 2005, at Amrita Vishwa Vidyapeetham, Coimbatore, pp. 434.
	4. "Audio Codec based on DFRCT", J. S. Bhat and Vijaya C, Proc. of NCC-2006, 27-29 th Jan. 2006, at IIT, New Delhi, pp.190-193.
	5. "Block Edge Detection in Discrete Fractional Fourier Transform Domain", J. S. Bhat and Vijaya C, Proc. of IEEE International Conference on Signal and Image Processing, vol.2, 7-9 th Dec. 2006, at B.V.B. College of Engineering and Technology, Hubli, pp. 772-776.
	6. "Implementation of DFRFT for Signal Compression", J. S. Bhat and Vijaya C, Proc. of 13 th NCC-2007, 26-28 th Jan. 2007, at IIT Kanpur, pp. 103-106
	7. "DCT Based Coloring of Gray Image", Vijaya C, J. S. Bhat, Proc. of National Conference on 'Convergence of Signal Processing, Communication & VLSI Design', 13 th -14 th August 2010, SDMCET, Dharwad

Conferences / seminars conducted	1. Faculty Development Program, 2007
	2. A one day workshop on 'signal Processing using Hybrid Architecture' Feb. 23, 2008 at SDMCET, Dharwad
	3. 3-day Workshop on 'Advances in Computer Network' for the students of E&CE, CSE, ISE, E&E, in Nov. 2008
	4. Workshop on 'Computer Awareness' for attenders of SDMCET, Dharwad, June/July 2008.
	5. One day workshop on 'Challenges for Women IT Professionals' in March 2009.
	6. Seminar on 'Introduction to Nanotechnology', on 09-05-2009
	7. Discussion on 'Solar Energy Projects', on 13-06-2009
	8. Seminar on 'Solar Energy Utilization' at SDMCET. Dharwad, 27-06-2009
	9. "Women's Day Celebration at SDMCET" on 12-03-2010
	10. National Conference on 'Convergence of Signal Processing, Communication & VLSI Design', 13 th -14 th August 2010, SDMCET, Dharwad
	11. "Women's Day Celebration at SDMCET" on 25-03-2011
Invited Talks	1. "Time-Frequency Representation", at S. J. M Institute of Technology, Chitradurga on 07-05-2004.
	2. "Applications of Mathematics in E&CE", at workshop on "Applications of Mathematics in Engineering", on 10-09-2005 held at SDMCET, Dharwad
	3. "Applications of Mathematics in E&CE", at workshop on "Applications of Mathematics in Engineering", in Jan. 2006, held at Bangalore.
	4. "Applications of Mathematics in E&CE", at one day workshop on Engineering Systems, 27-10-2007, BIET, Davanagere.
	5. "Transforms in Image Processing", March 2009 at a workshop on 'Recent Research Trends in Image Processing', held at SDMCET, Dharwad
	6. "Fundamentals of Image Processing", March 2009, at a workshop on 'Image Processing', held at SDMCET, Dharwad
	7. "Fundamentals of Image Processing", Feb. 2010, at a workshop on 'Image Processing', held at JMMCOE, Jaysingpur, Maharashtra
	8. "Mathematics in Signal Processing", April 2011, at a Seminar on '', held at KCD, Dharwad
Chaired a session	IEEE International Conference on Signal and Image Processing, 7-9 th Dec. 2006, at B.V.B. College of Engineering and Technology, Hubli.
At VTU Level	Special Invitee for BOS meeting held in SJCE, Mysore on 31-01-2009.
	Faculty in EDUSAT Program for DSP Algorithms And Architecture, VII Semester, E&CE, Oct-Nov. 2009
	Faculty in EDUSAT Program for Analog & Mixed Mode VLSI Design, VI Semester, E&CE, April 2010
Reviewer	Reviewer for IEEE Signal Processing Letters, 2009
	Reviewer for International Conference ICETE-11 held at NMAMIT, Nitte, May 2011
	Program Committee Member for International Conference ARTCOM-11 to be held at Reva College of Engineering & Technology, Bangalore, during Sept. 2011
	Reviewer for International Journal of Electronics, August 2012.
	Invitation as Editor, Bioinfo Publications Journals, Navi Mumbai Sept 2012
Reviewer for International Journal of Speech Technology, August 2012	
Any other	Committee member in staff recruitment process at USIC (University Scientific Instrument Centre), Karnatak University, Dharwad, Jan. 2011